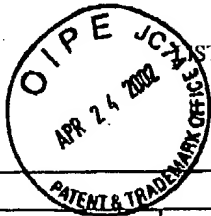


Form 1449

U.S. Department of Commerce
Patent and Trademark OfficeDocket No.
Honeywell-
H0002453USSerial No.
10928,512LIST OF PRIOR ART CITED BY APPLICANT
(use several sheets if necessary)Applicant(s):
STANLEY A. WHITEFiling Date
August 13, 2001Group Art Unit
2124

U.S. PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
<i>[Signature]</i>	AA	5,675,498	OCT. 7, 1997	White			
<i>[Signature]</i>	AB	5,577,073	NOV. 19, 1996	White			
<i>[Signature]</i>	AC	5,444,639	AUG. 22, 1995	White			
<i>[Signature]</i>	AD	5,587,910	DEC. 24, 1996	Woodard			
	AE						
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FOREIGN PATENT DOCUMENTS

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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AQ	A. H. Gray, Jr. & J. D. Markel, "DIGITAL LATTICE AND LADDER FILTER SYNTHESIS", IEEE Transactions on Audio & Electro-Acoustics, Vol. AU 21, No. 6, December (1973), pgs. 491-500.
	AR	P. A. REGALIA, et al., "THE DIGITAL ALL-PASS FILTER: A VERSATILE SIGNAL PROCESSING BUILDING BLOCK", Proceedings of the IEEE, Vol. 76, No. 1, January (1988), pgs. 19-37.
<i>[Signature]</i>	AS	R. E. BEST, "PHASE-LOCKED LOOPS", Design, Simulation, & Applications, Fourth Edition, Chapter 5, McGraw Hill, pgs. 229-249.
	AT	
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	AV	
	AW	

Examiner *D.H. Malzahn*Date Considered *7/13/04* *8/4/04*

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P02,0154

Serial No.

09/928,512

INFORMATION DISCLOSURE CITATION
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Applicant
Stanley A. White

Filing Date
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Group Art Unit
2121

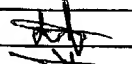
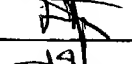
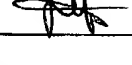
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Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
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	AK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
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Examiner

D.H. Malzahn

Date Considered

8/4/04

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